FIG. 1 - CypherNET Block Diagram

connection to
protected network

200   204

connection to
unprotected network

Non-ATM Local
Interface Subsystems

205

Physical Interface

Ethernet
Frame Relay
Bit Stream
Logic

SONET/SDH or ATM
Local Interface
Subsystems

Physical Interface

SONET/SDH
or ATM UNI
Processor

202

SONET/SDH or ATM
Network Interface
Subsystems

Physical Interface

SONET/SDH
or ATM UNI
Processor

203

Non-ATM Network
Interface Subsystems

201

Physical Interface

Ethernet
Frame Relay
Bit Stream
Logic

High-Speed Crypto Subsystem

210

SONET/SDH or
ATM Processor

211

220

221

High-speed IPSec
Crypto Engine Subsystem

SONET/SDH or
ATM Processor

212

241

232

140

MPC8260 Microprocessor

242

| Ethernet Interface | RS232 Interface | Status & Display | Keypad | USB Socket | Low-Speed Crypto Subsystem | Software Crypto Subsystem | Real Time Clock | Noise Source |

243   **Management Subsystem**   244   245

247   246

240

250
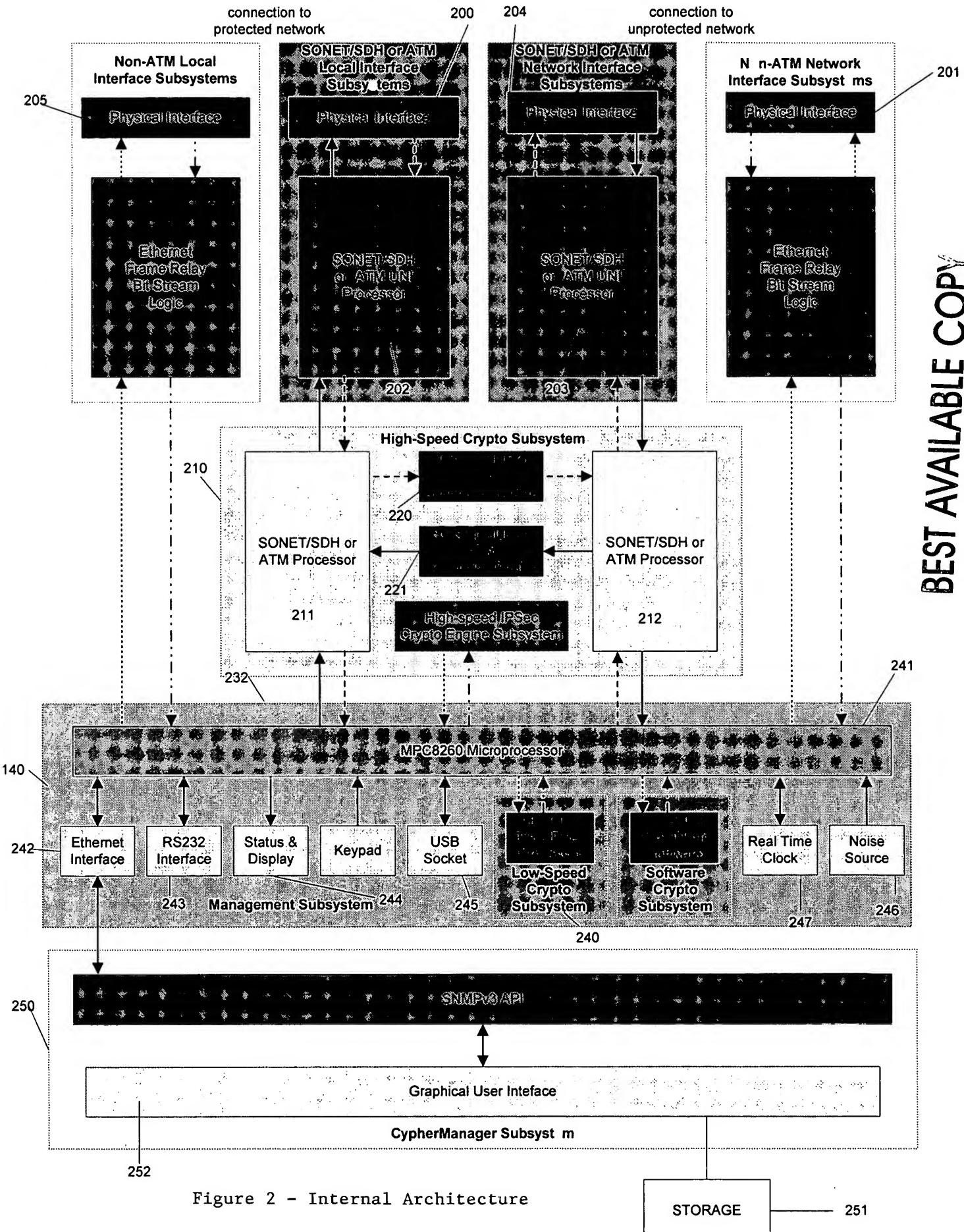
SNMPv3 API

Graphical User Inteface

CypherManager Subsystem

252

STORAGE   251

Figure 2 - Internal Architecture